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(54) ANALOG-TO-DIGITAL CONVERTER

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(57) Abstract:

PURPOSE: To obtain an A/D converter with simple constitution which can read directly a signal level in an digital value and excels in stability with low cost, by securing the correspondence between the initial charging potential and the discharging time of a charging/discharging circuit.

CONSTITUTION: A switch 2 is closed by a timing genetating circuit 8, and an input signal is charged to a capacitor 3. Then a switch 5 is closed and the electric charge stored in a capacitor 3 is discharged at a fixed discharging constant via a resistor 4. The discharging time is measured by a comparator 6 and a counter 9. Then the discharging time is set opposite to the dB value of the charging potential by a converting circuit 10 to obtain a digital (dB) output corresponding to the input analog voltage. The correction voltage 11 is used as the input voltage in order to perform a correcting action prior to a normal action. Then the corresponding relation is previously corrected by a control circuit 15 between the circuit 10 and an input level 1dB.

